The following undocumented mnemonic codes for PMA have been found.

64V-mode:
PZE addr generate an instruction with an opcode of zero (so that it can be added later). Generates 1 or 2 word instruction as appropriate.

32I-mode:
HRR R,n Half-register Right Rotate.  \((R = \text{reg no.}, n = \text{no. of bits})\)
Similarly RRR,RLA etc. where:
H/R = half or full register
L/R = left or right shift
A/R/L/ = arithmetic, rotate or logical shift

General:
DXA =E16S
EXA =E32S
RMP =RMC
AOA =A1A
SOA

Unknowns:
ISI
OSI